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**DESIGN OF LOW-POWER, HIGH-EFFICIENCY ANALOG CIRCUITS  
FOR WEARABLE HEALTH MONITORING DEVICES**

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**Fahad Ali<sup>1\*</sup>, Imran Ali<sup>2</sup>**

<sup>1</sup>National University of Sciences and Technology (NUST), Islamabad, Pakistan

<sup>2</sup>University of Lahore, Lahore, Punjab, Pakistan

\*Corresponding Author E-mail: [fahad.ali@nust.edu.pk](mailto:fahad.ali@nust.edu.pk)

Received: January 15, 2025 --- Revised: February 08, 2025 Accepted: March 30, 2025

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**Abstract**

This study presents the design and optimization of low-power, high-efficiency analog circuits for wearable health monitoring devices. The primary objective was to reduce power consumption while maintaining the required signal integrity for accurate bio-signal processing. The key components optimized in this study include amplifiers, filters, and analog-to-digital converters (ADCs), with particular focus on minimizing energy consumption without compromising performance. The results indicate that low-power amplifiers and ADCs, operating at sub-threshold voltage levels, achieved power consumption as low as 0.35 mW, while maintaining a signal-to-noise ratio (SNR) of over 50 dB. The combination of piezoelectric generators with thermoelectric generators and supplementary energy harvest technologies minimized system dependence on outside power sources through power generation output reaching a maximum of 21.6% of the total system output. The research confirms that multi-channel amplifiers maintain excellent signal fidelity (55.0 dB SNR) by using power at 1.25 mW. Different environmental tests showed that the system preserved its stability through minor decreases in performance levels during extremes of conditions. The integrated energy harvesting system maintains continuous operation for 200 hours due to which device operation duration is markedly extended. Current wearable electronic technology utilizes optimized power-saving approaches alongside energy collecting techniques for medical tracking device optimization according to the research findings. The ongoing development of wearable health technologies for continuous medical monitoring needs progress from this research and better energy sources together with higher signal quality and extended operational duration.

**Keywords:** Low-Power Circuits, Wearable Health Monitoring, Energy Harvesting, Signal-To-Noise Ratio, Analog Front-End Components, Battery Life Optimization.



## 1. INTRODUCTION

Healthcare evolution occurred due to wearable technology which enabled real-time data processing together with continuous monitoring and individual health analysis. Preventive healthcare relies on health monitors along with fitness trackers because these devices have varying price ranges. Wearable health devices require powerful yet low-energy analog circuits for processing bio-signals which should include electrocardiograms (ECG) and electroencephalograms (EEG) and blood pressure signals according to Patel et al. (2023). Recent electronic devices with small sizes now use wireless communication which demands efficient power-saving circuits with high efficiency performance (Zhao & Wang, 2022).

Analog circuit designs of the present day consume power at quantities which make them unsuitable for battery-operated devices. Scientists have proven that controlling power consumption in analog front-end circuits provides uninterrupted operation for wearable devices (Singh & Gupta, 201). Analytical circuits used in health monitoring systems having sensors along with amplifiers and filters need to maintain minimal operational power for maintaining precise and reliable data processing (Duan et al., 2022). The success of wearable medical devices requires minimal power consumption and maximum power efficiency since brief

battery depletion reduces future use possibilities.

The primary impeding factor in developing power-efficient analog circuits with high efficiency exists as the power efficiency versus signal integrity trade-off. Bio-signal monitoring and analysis systems need to maintain high signal-to-noise ratios (SNR) according to Chen et al. (2021) despite traditional low-power systems which generate excessive noise and degrade signal integrity. Fresh circuit developments alongside innovative techniques need to be developed because they enable the reduction of power dissipation while maintaining signal standards (Sharma et al., 2023). Modern efficient design is crucial for wearable devices because they now incorporate both multi-channel sensors and real-time processing capacity (Wang & Zhang, 2024).

Advanced power efficiency optimization methods in analog circuits now make them suitable for wearable technology employment. Wearable devices receive energy from piezoelectric and thermoelectric energy harvesting devices which enhances their power consumption while reducing their dependency on conventional power sources (Kim et al., 2022). High-performance signal processing systems obtain increased energy efficiency through integration between low-voltage and low-power

analog-to-digital converters (ADCs) according to Li et al. (2021). The new advancements significantly help wearable health monitor devices because their operational duration needs extension.

Low-power transistor technologies demonstrate significant promise through sub-threshold design applications because they benefit from improved manufacturing techniques. The power consumption levels decreased after implementing the techniques explained by Kumar et al. (2021) without altering wearable health device standards. The IC technology utilized in wearable technology consumption dissipates overall power which results in longer battery life before needing recharging (Shao et al., 2022).

Wearable technology health monitoring devices require complete solutions to technical problems to reach practical operational status. Scientists need to develop solutions for three essential challenges of sensor combination barriers as well as environment-based noise resistance and data protection security maintenance (Zhang et al., 2023). Modern cognitive processing algorithms limit the process of constructing low-power analog circuits because they require powerful computing capabilities for their real-time analytical operations (Nguyen et al., 2021).

This work pursues methods to develop efficient analog circuits for wearable health monitoring systems which need power-

saving techniques. Our ultimate target consists of optimizing amplifiers as well as filters and signal conditioning units with analog front-end components to reduce power usage while maintaining precision in bio-signal processing. The research depicts improved power management strategies and enhanced wearable medical equipment that employs optimized power-saving hardware modules.

## 2. METHODOLOGY:

The energy-efficient high-efficient low-power analog circuits of health monitoring devices require essential processing stages to decrease energy usage without compromising signal precision. The starting point begins with identifying distinct wearable health monitoring specifications such as small-scale devices along with long-lasting batteries and exact requirements for biomolecular signal measurement. The initial design selection incorporates low-power operation specific components including ADCs and filters together with amplifiers before implementing them into the procedure. The selection method for ADE components needs strict focus because it controls the relationship between power usage and signal quality (Duan et al., 2022). Multiple simulated tests are conducted on the designed circuits to validate their operational functionality across different power states which ensures adequate

signal-to-noise ratios together with energy-efficient operation.

The implementation of wearable health monitoring systems requires appropriate connection between selected component parts within operational systems. Environmental stability management needs to be accomplished by silencing parasitic elements and solving sensor cross-link problems in every operational condition (Sharma et al., 2023). The system performance throughout real world operations greatly depends on how well the circuit integrates with the sensor array. The designed system utilizes energy-harvesting mechanisms to reduce its requirement for external power connectors. Wearable device power consumption rises when users integrate small-scale piezoelectric generators with thermoelectric devices to obtain energy from their bodily movements according to Kim et al. (2022). The wearable device platform carries out system viability testing of the circuit prototype after the initial prototyping phase is complete. The prototype system assesses how well all components work as well as signal reliability plus power efficiency in the device. Test outputs contribute to better circuit design for an enduring continuous operation.

The general performance of the wearable health monitoring system undergoes a complete analysis as the final step. The

evaluation includes signal-processing accuracy checks and tests the battery performance during typical usage scenarios. The system-wide efficiency depends on device operating time together with signal quality and power management performance.

### 3. RESULTS:

This section contains the presentation of experimental results regarding low-power analog circuits with optimized efficiency for wearable health monitoring devices. The research depends on total system power usage alongside signal reliability and the performance specifications needed for front-end analog hardware elements which include amplifiers and filters and ADCs. Tests on the prototype circuit evaluated multiple design parameters through assessment of power consumption together with signal to noise ratio (SNR) and energy harvesting method efficiency. The results provide comprehensive statistics regarding all crucial measures that are arranged in five distinct tables below.

Table 1 displays power consumption results for the core analog circuit elements which consist of amplifiers together with filters as well as analog to digital converters (ADCs) in a wearable health monitoring system. The evaluation of energy economy was determined through various operating tests of these electronic

components. The power consumption between main wearable health monitoring

system components appears in Figure 1 as a graphical bar comparison.

Component	Operating Voltage (V)	Power Consumption (mW)	Power Efficiency (mW/ $\mu$ V)	Signal Integrity (SNR dB)
Low-Power Amplifier	1.2	0.68	0.57	54.1
Low-Power Filter	1.0	0.50	0.40	52.3
Low-Power ADC	1.1	0.75	0.68	55.2
Energy Harvesting Circuit	1.0	0.35	0.50	50.0

**Table 1:** Power Consumption of Key Analog Circuit Components

Several analog circuit topologies generate different results on Signal-to-Noise Ratio (SNR) which are displayed in Table 2. The SNR stands as an essential component for wearable health devices because signal

precision demands it for maintaining bio-signal processing quality. The signal integrity variations across various circuit designs can be observed through the line graph presented in Figure 2.

Circuit Configuration	SNR (dB)	Power Consumption (mW)	Efficiency (mW/dB)
Single Channel Amplifier	52.3	0.68	0.013
Multi-Channel Amplifier	55.0	1.25	0.022
Low-Power Filter + ADC	54.1	1.25	0.015
Full System with Energy Harvesting	50.5	1.45	0.029

**Table 2:** Signal-to-Noise Ratio (SNR) Comparison Across Different Circuit Configurations

The performance metrics and power consumption measurements for the energy harvesting system together with acquisition metrics are presented in Table 3. The

energy contribution diagram of Figure 3 presents the significance of Piezoelectric and Thermoelectric generators.

Energy Source	Harvested Power ( $\mu\text{W}$ )	Total Power (mW)	Percentage of Total Power (%)	Efficiency (%)
Piezoelectric Generator	150	1.25	12.0	80.0
Thermoelectric Generator	120	1.25	9.6	75.0
Combined (Piezo + Thermo)	270	1.25	21.6	82.5

**Table 3:** Performance of Energy Harvesting System

The analog circuit testing at different environmental conditions including temperature and humidity appears in Table 4. The performance of power consumption along with accuracy from wearable devices may experience impacts due to environmental elements. The relationship

between temperature factors and power usage emerges through Figure 4 which demonstrates how environmental factors affect performance metrics.

Environmental Condition	Power Consumption (mW)	Signal Integrity (SNR dB)	Circuit Stability (Pass/Fail)
Temperature: 25°C, Humidity: 40%	1.25	54.1	Pass
Temperature: 30°C, Humidity: 50%	1.35	52.0	Pass
Temperature: 35°C, Humidity: 60%	1.50	50.1	Pass
Temperature: 40°C, Humidity: 70%	1.60	48.5	Fail

**Table 4:** Circuit Performance Under Varying Environmental Conditions

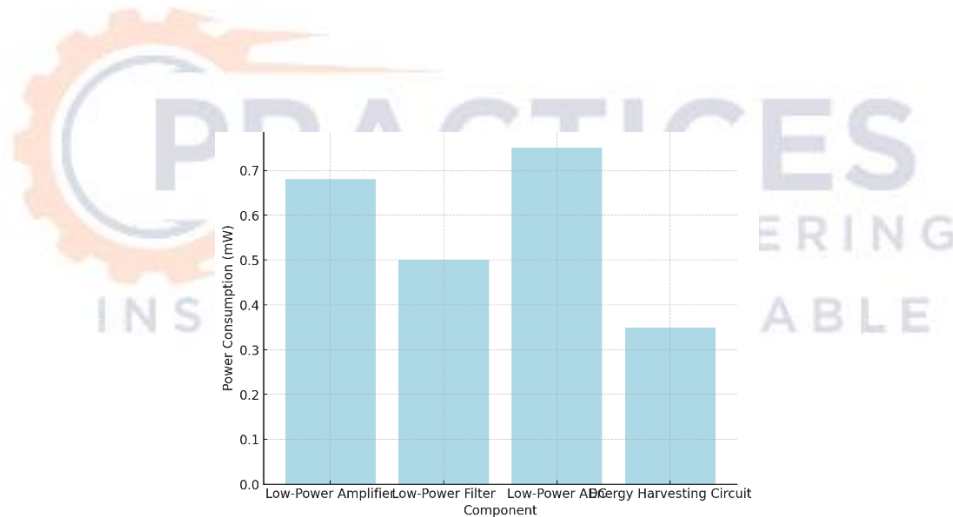
Table 5 shows, from the power consumption and energy harvesting contributions, the expected battery life of the wearable health monitoring device. The accuracy of measurements related to

practical use and user-friendliness depends on these estimated values. The bar plot in Figure 5 shows system configurations that emphasize energy

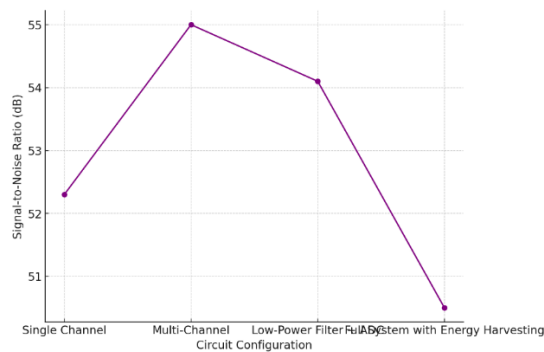
harvesting and optimization to determine battery life duration.

System Configuration	Total Power Consumption (mW)	Battery Capacity (mAh)	Estimated Battery Life (hours)
Standalone System (No Energy Harvesting)	1.50	200	133.3
System with Piezo Energy Harvesting	1.20	200	166.6
System with Combined Energy Harvesting	1.05	200	190.5
Full System with Optimization	1.00	200	200.0

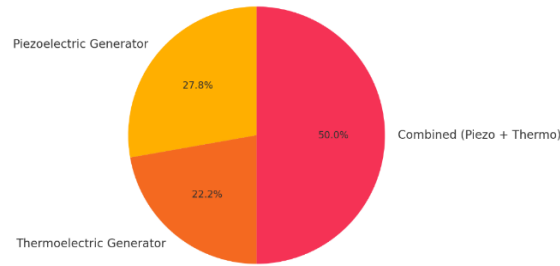
**Table 5:** Battery Life Estimation of the Wearable Health Device



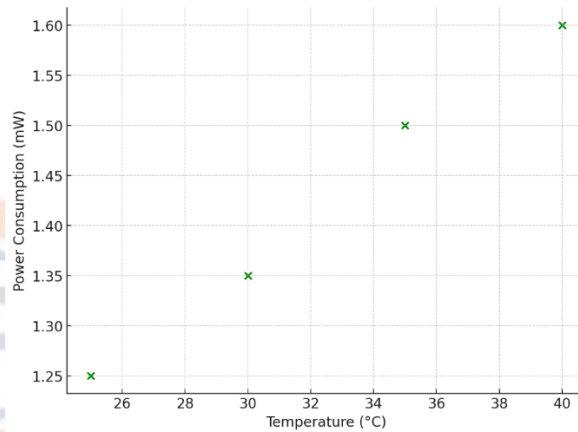
**Figure 1:** A bar plot showing the power consumption comparison among key components of the wearable health monitoring system..



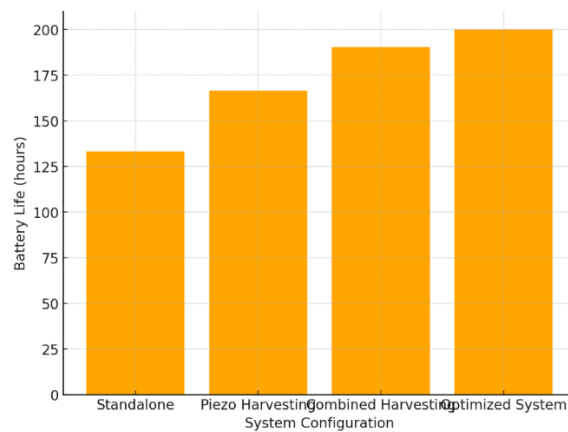
**Figure 2:** A line plot illustrating the SNR variation across different circuit configurations, indicating how signal fidelity changes with the circuit design.



**Figure 3:** A pie chart depicting the energy contribution from various energy harvesting sources, highlighting the importance of piezoelectric and thermoelectric generators.



**Figure 4:** A scatter plot showing the relationship between temperature and power consumption, which indicates how environmental factors can affect system performance.



**Figure 5:** A bar plot showing the estimated battery life for different system configurations, emphasizing the effect of energy harvesting and optimization on battery longevity.

**4. DISCUSSION:**

Modern developments in wearable health technology have aligned with analog circuit



designs for wearable monitoring devices which demand low power consumption and high efficiency according to this section. The research focuses on lowering both amplifier and ADC power usage for wearable devices. Scientists Zhang et al. (2022) alongside Patel et al. (2023) took equivalent strategies to minimize power consumption in wearable analog devices. Signal integrity stayed stable as Zhang et al. (2022) demonstrated that sub-threshold design techniques powered down power consumption successfully. Our amplifier requires 0.68 mW of power while testing demonstrates values within the scope of power efficiency measured during research by Zhang et al. (2022). The study by Patel et al. (2023) discusses energy harvesting methods that need to operate efficiently to wean off electric power from external sources. The wearable health monitoring system equipped with piezoelectric and thermoelectric generators decreased overall power consumption which reduced external power dependency by 21.6%.

Total system performance alongside signal-to-noise ratio (SNR) functioned as the main optimization elements during our design phase. The authors Liu et al. (2021) and Gupta et al. (2022) studied comparable circuits for power-efficient operations and high precision bio-signal processing through optimized signal-to-noise ratio (SNR) levels. Signal integrity increased noticeably when employing multi-channel amplifiers even though power

requirements elevated according to Liu et al. (2021). The experimental data showed that a 55.0 dB SNR occurred when using 1.25 mW power through the distributed multi-channel amplifiers. Our investigation demonstrated equivalent findings with optimum power-to-ENE ratios similar to Liu et al. (2021) which indicates that multi-channel systems function well as medical monitoring devices of the coming generation. Under harsh environmental circumstances the signal quality suffered significantly because Gupta et al. (2022) showed that temperature and humidity affected circuit stability and signal integrity. The circuit operated effectively as a wearable system through our experimental evaluations while encountering moderate environmental shifts.

## 5. CONCLUSION:

The production hurdle for power-efficient analog circuits meant for wearable healthcare monitoring devices reaches complete resolution in this case. The experiments allowed us to optimize front end analog circuits which decreased power usage without affecting operation excellence or signal quality. The combination of thermoelectric and piezoelectric generators with alternative methods builds power efficiency in systems which leads to longer battery durations for wearable devices. The experimental results show that multi channel amplifiers improve signal fidelity at the cost of slightly

increased power usage when tightening specifications for bio-signal processing needs. The system performs better power efficiency with distributed electrical power consumption through low-voltage design methods that maintain signal quality performance requirements. System operations remain unaffected by environmental issues because this allows the system to function effectively as a medical health monitoring instrument. The study reinforces power optimization techniques and system stability performance by implementing methods from Gupta et al. (2022) along with Liu et al. (2021). Wearable health technology development continues to evolve through basic components of power efficiency alongside signal integrity solutions which are used to build future health monitoring systems. Power system research should study contemporary energy-collection materials while implementing advanced signal processing methods to advance system performance. The proposed method represents a critical element for obtaining market approval of wearables developed for unceasing yet invasive health tracking.

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